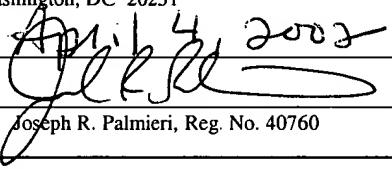




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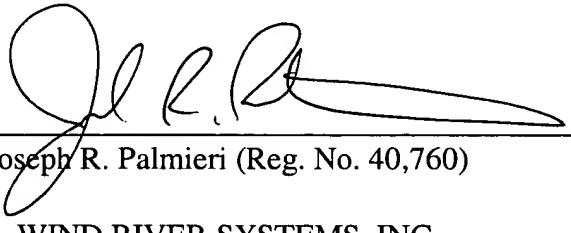
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
INFORMATION DISCLOSURE STATEMENT		Attorney Docket Number: 218.1029	
Application Number 09/916,957	Filing Date 7/27/01	Examiner	Group Art Unit 2183
Invention Title Peripheral Failover System		Inventor(s) Manish Sinha, et al.	

Address to:
Assistant Commissioner for Patents
Washington D.C. 20231

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service on the date listed below with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.	
Date:	April 16 2002
Signature:	
Joseph R. Palmieri, Reg. No. 40760	

- I. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant hereby bring the following reference(s), listed on the attached modified PTO Form No. 1449, to the attention of the Examiner. It is respectfully requested that the reference(s) be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- II. A copy of each patent, publication or other information listed on the modified PTO Form No. 1449 is enclosed.

Dated: April 4, 2002

By: 

Joseph R. Palmieri (Reg. No. 40,760)

WIND RIVER SYSTEMS, INC.
500 Wind River Way
Alameda, CA 94501
510-749-2667
510-749-2255 (fax)